



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2631

#580

Re application of: David Robert Cameron Rolston *et al.*

Serial No: 09/773,246

Art Unit: 2631

Filed: February 1, 2001

Examiner: n/a

Subject: METHOD AND APPARATUS FOR DISTRIBUTED SYNCHRONOUS
CLOCKING

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INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.97(b)

Sir:

To comply with applicants' duty of disclosure under 37 CFR 1.56, Form PTO-1449, listing documents known to applicant, is submitted herewith.

- ☒ Copies of the listed documents are enclosed.
- ☐ This is a continuing application and copies of the documents were submitted in respect of the parent application No.

Relevance of information not in English - 37 CFR 1.98(a)(3)

- ☐ To comply with 37 CFR 1.98(a)(3), EITHER a copy of US patent number is submitted herewith, such US patent being cited in a family of patents corresponding to patent number listed in the form PTO 1449. OR an English language abstract is appended to ... patent No. (document AX)
- ☐ Document # discloses
- ☐ The relevance of is discussed in the specification.

The submission of any document herewith, which is not a statutory bar, is not intended as an admission that such document constitutes prior art against the claims of the present application. Applicant does not waive any rights to take any action which would be appropriate to antedate or otherwise remove as a competent reference any document which is determined to be a *prima facie* prior art reference against the claims of the present application.

Applicant respectfully requests that the listed documents be considered by the Examiner and be made of record in the present application and that an initialled copy of form PTO-1449 be returned in accordance with MPEP Sec. 609.

The Commissioner is hereby authorized to charge any fee required to Deposit Account No. 20-0771.

Respectfully submitted

THOMAS ADAMS
Reg. No. 31078

DATE: June 26, 2001

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Docket No. AP566US

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Complete if Known

Application Number	09/773,246
Filing Date	February 1, 2001
First Named Inventor	David Robert Cameron Rolston
Group Art Unit	2631
Examiner Name	
Attorney Docket Number	AP566US

Sheet	1	of	2	Attorney Docket Number	AP566US
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[illegible][illegible]

Date	
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Substitute for form 1449B/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	09/773,246
		Filing Date	February 1, 2001
		First Named Inventor	David Robert Cameron Rolston
		Group Art Unit	2631
		Examiner Name	
		Attorney Docket Number	AP566US
Sheet	2	of	2

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	2A	"A PLL Clock Generator with 5 to 110 MHz of Lock Range for Microprocessors", by I.A. Young, J.K. Greason, & K.L. Wong, in IEEE Journal of Solid-State Circuits, Vol. 27, No. 11, November 1992, pp. 1599-1606.	
	2B	"Simultaneous Bidirectional Receiver Logic", by Kanichi Ishibashi et al. in IEEE Micro, Jan.-Feb. 1999, pp. 14-18.	
	2C	"Scaling Optoelectronic-VLSI Circuits into the 21st Century: A Technology Roadmap", by A.V. Krishnamoorthy & D.A. Miller, in IEEE J. of Selected Topics in Quantum Electronics, Vol. 2, No. 1, April 1996, pp. 55-76.	
	2D	"Optical Receivers for Optoelectronic VLSI", by T.K. Woodward, A.V. Krishnamoorthy, A.L. Lentine & L.M.F. Chirovski, in IEEE J. of Selected Topics in Quantum Electronics, Vol. 2, No. 1, April 1996, pp. 106-116.	
	2E	"Optical Control of Distributed Actuator and Sensor Arrays" by B. Bamieh, F. Paganini and M. Dahleh, in SPIE, Vol. 3323, March 1998, pp. 467-478.	
	2F	"Optical clock distribution to silicon chips" by B.D. Clymer and J.W. Goodman, in Optical Engineering, Vol. 25, No. 10, October 1986, pp. 1103-1108.	
	2G	"A Synchronous Approach for Clocking VLSI Systems" by F. Anceau, in IEEE Journal of Solid-State Circuits, Vol. SC-17, No. 1, February 1982, pp. 51-56.	
	2H	"Clock Distribution Using Coupled Oscillators", by I. Galton, D.A. Towne, J.J. Rosenberg and H.T. Jensen, in IEEE, 0-7803-3073-0-96, 1996, pp. 217-220.	
	2I	"The Composite Banyan Network" by S-W Seo and T-Y Feng, in IEEE Transactions on Parallel and Distributed Systems, Vol. 6, No. 10, October 1995, pp. 1043-1053.	

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¹EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

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